

Exhibit A Wood

U.S. Patent Apr. 16, 1985 Sheet 1 of 3 4,511,815

FIG. 1.

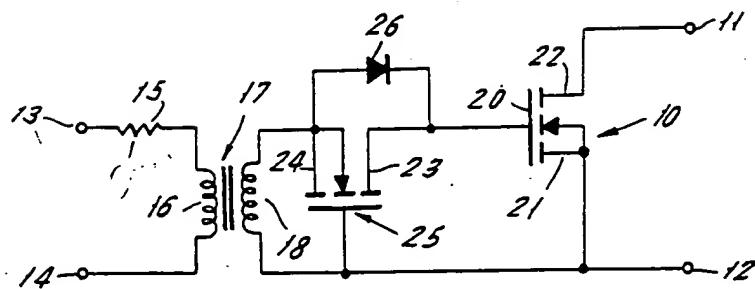
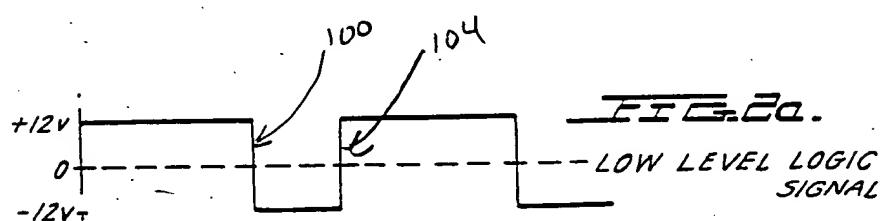
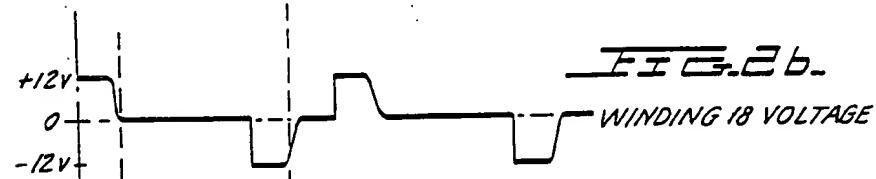


FIG. 2a.



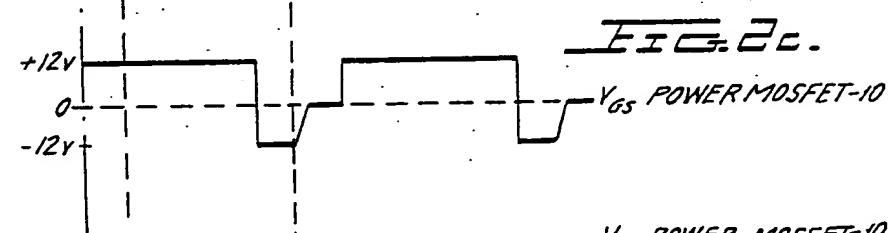
LOW LEVEL LOGIC SIGNAL

FIG. 2b.



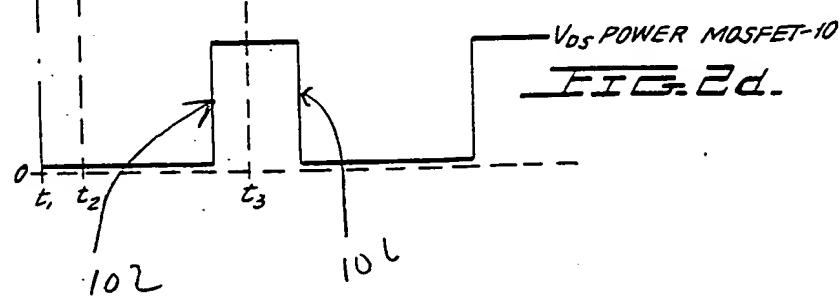
WINDING 18 VOLTAGE

FIG. 2c.



$V_{GS}$  POWER MOSFET-10

FIG. 2d.



$V_{DS}$  POWER MOSFET-10